

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: See Attached Schedule A

Serial No.: See Attached Schedule A

Filed: See Attached Schedule A

For: See Attached Schedule A Attorney Docket No: See Attached  
Schedule A

REVOCATION AND POWER OF ATTORNEY

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

S I R:

**Lovoltech Inc.**, owner of the entire right, title and interest in, to and under the inventions listed in the attached Schedule A, hereby revokes all previous powers of attorney and appoints the registered attorneys at Morgan, Lewis & Bockius LLP associated with Customer No. **24341** as its attorneys to prosecute these matters, and to transact all business in the Patent and Trademark Office connected therewith, said appointment to be to the exclusion of the inventors and their attorney(s) in accordance with the provisions of 37 C.F.R. 3.71, provided that, if any one of these attorneys ceases being affiliated with the law firm of Morgan, Lewis & Bockius LLP as partner, counsel, or employee, then the appointment of that attorney and all powers derived therefrom shall terminate on the date such attorney ceases being so affiliated.

Please direct all future correspondence to the address associated with Customer No. **24341**, Morgan, Lewis & Bockius LLP and direct all telephone calls to Morgan, Lewis & Bockius LLP at (650) 843-4000.

Assignee:	Lovoltech Inc. (now Ospeed Semiconductor)
Date: <u>September 29, 2006</u>	Signature: <u>Richard Francis</u>
	Typed Name: <u>RICHARD FRANCIS</u>
	Position/Title: <u>C. T. O.</u>
Address:	3970 Freedom Circle, Suite 101 Santa Clara, CA 95054

## SCHEDULE A

Serial No./ Patent No.	Filing Date/ Issued Date	Title	First Named Inventor	Docket No.
10/728,449	December 5, 2003	Flip-Chip Packaging	William Planey	65860-5001-US
10/776,487	February 10, 2004	Method for a Junction Field Effect Transistor with Reduced Gate Capacitance	Ho-Yuan Yu	65860-5002-US
10/816,980	April 2, 2004	Method and Structure for Composite Trench File	Ho-Yuan Yu	65860-5003-US
10/869,718	June 15, 2004	Schottky Barrier Rectifier and Method of Manufacturing the Same	Ho-Yuan Yu	65860-5004-US
11/023,272	December 22, 2004	Method of Manufacturing a Schottky Barrier Rectifier	Ho-Yuan Yu	65860-5005-US
11/149,718	June 10, 2005	MOSFET Having a JFET Embedded as a Body Diode	Jian Li	65860-5006-US